Specifications

Products Name	Low Resistance Chip Resistor
Product Series	FLRV11050FCR***F
Classification	Generic specification



FLRV11050F Low Resistance Chip Resistor Specification

1. Scope

This specification applies to FLRV11050F Series Low Resistance Chip Resistor for use in electric equipment.

2. Part number

<u>R***</u> FLR <u>V11050</u> <u>F</u> <u>C</u> <u>F</u>

(1)

(2)

(4) (3)

(5)

(6)

(1) Product series

(2) Size

(4) Characteristic type

(5) Nominal Resistance

(6) Resistance tolerance

(3) Electrode type

High operating temperature type

Short-side electrode 11.0*5.0mm size

(example) $10m\Omega \rightarrow R010$

Foil Low Resistance

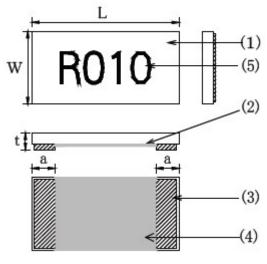
Face-down type

F (±1.0%) G(±2.0%) J(±5.0%)

3. Structure

The ceramic substrate is adhered to the metal foil (Ni-Cu) resistive element; terminals are formed on top of the foil.

4. Dimensions



No.	Components	Material / Process
(1)	Substrate	Alumina 96%
(2)	Resistor	Ni-Cu alloy
(3)	Terminals	Plated Sn+Ni (on Cu)
(4)	Protection coat	Epoxy resin (Green)
(5)	Marking	Epoxy resin (Black)

Symbol	Dimensi	ions (mm)
	11	1.00±0.20
W	5	.00±0.20
а	$8m \Omega \sim : 2.36 \pm 0.30$ 5-7m $\Omega : 3.60 \pm 0.30$	
t	0.65±0.20	

5. Marking

Resistance value code is marked on the top surface.

Example) $10m\Omega$ -> R010

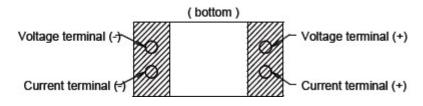
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6. Schematic diagram, Measurement point

Schematic diagram

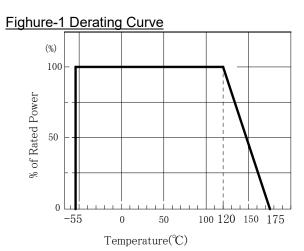
Measurement point





7. Specification

_ opcomeation		
Parameter	Specification	
Resistance Value	5m to $1,000$ m Ω (for standard resistances)	
Resistance Tolerance	F (±1.0%)	
Temperature Coefficient of Resistance	±50ppm/°C	
Rated Load	5.0W	
Operating Temperature Range	-55~+175°C	Refer to Derating curve, Figure-1
Rated ambient temperature	+120°C	
Rated Voltage	√ (Power×Res	sistance) (V)
Maximum Over Current	√ (Power×Resistance) (V) which smaller between √ (96W÷resistance) & 120A 10msec on, 60sec off in maximum of 10 times with recommended footprint set forth hereunder.	



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8. Reliability testing

Test Item	Test Condition	ons	Specification
Short Time Over Load	Voltage of 1.5 times the rated voltag	e shall be applied for 5S.	±(1.0%+0.0005Ω)
Load life	Rated voltage for 90 min followed by a pause of 30 min at a temperature of 70±3°C. Cycles shall be repeated for 1000h.		±(2.0%+0.0005Ω)
Moisture Load life	Rated voltage for 90 min followed by a pause of 30 min at a temperature of 60±2°C with relative humidity of 90%. Cycles shall be repeated for 1000h.		±(2.0%+0.0005Ω)
Temperature	[-55°C 30 min -> R.T. 3 min ->	100 continuous cycles	±(1.0%+0.0005Ω)
Cycle	+155°C 30 min -> R.T. 3 min]	1000 continuous cycle	±(2.0% +0.0005Ω)
Soldering Hearting	260±5°C solder, 10±1sec dip		±(1.0%+0.0005Ω)
Substrate Bending	Test board length: 90mm Bend depth: 2mm Test board: Glass-Epoxy t=1.6mm		±(1.0%+0.0005Ω)
Solderability	245±5°C solder, 3+1/-0 sec dip.		A new solder shall cover minimum of 90%

9. Packaging

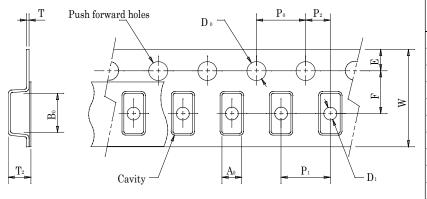
Packing quantity: 1,000 pieces/reel.

Tape diagram/dimension	Figure-2
Peeling strength of seal tape	Figure-3
Reel form · Labeling position	Figure-4
Taping direction	Figure-5

Marking The following items shall be printed on the reel label. (Figure-6)

Part number
Quantity for each reel
Manufacturing month code
Manufacturer
Inspection number (Lot number)
The country of origin
Lead free designation by double broken line

Figure-2 Tape diagram/dimension: Material: Plastic



A0	5.40±0.10
В0	11.50±0.10
W	24.00±0.30
F	11.50±0.10
Ε	1.75±0.10
P0	4.00±0.10
P1	8.00±0.10
P2	2.00±0.10
D0	1.50±0.10
D1	1.50±0.10
Τ	0.30±0.05
T2	1 20+0 15

Symbol

Dimensions

(mm)

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Pulling direction

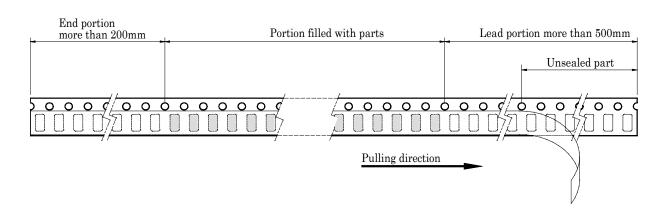


Figure-3 Peeling strength of seal tape:

 $F = \text{peeling strength: } 0.1 \sim 0.7 \text{N } (10 \sim 71 \text{gf})$

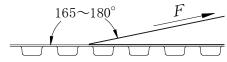
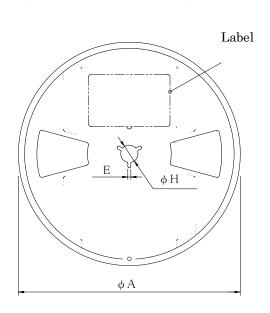
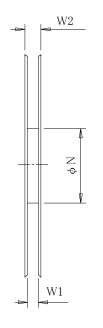


Figure-4 Reel form · Labeling position:

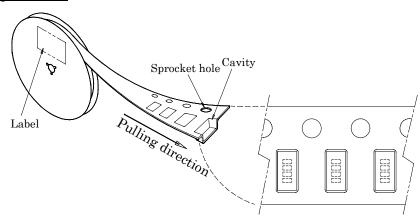




Symbol	Dimensions T1
ΦА	180+0.0/-3.0
ΦН	13.0±0.2
ΦΝ	60±1.0
E	2.0±0.5
W1	25.4±1.0
W2	29.4±1.0

(unit:mm)
Material: Plastic

Figure-5 Taping direction:



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Figure-6 Label contents:

FLRV11050FCR020F

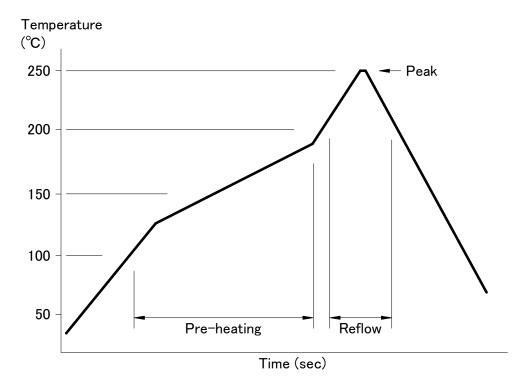
Q.T.Y 1,000 [PCS] INSPECTED **u** Y.E.D CO.,LTD. 90917010 MADE IN JAPAN

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Part number
Quantity for each reel
Manufacturing month code
Manufacturer
Inspection number (Lot number)
The country of origin
Lead free designation by double broken line

- 10. Recommended implementation temperature profile
- 10.1 Reflow temperature profile

Twice reflows are allowed by the following temperature profile



Surface temperature of resistance and time

		tollico ollica tillic
Pre-heating	130~180°C	60~90sec
Reflow	220°C Max	30~90sec
Peak	240~260°C	10sec Max

10.2 Flow temperature profile

Less than 260°C 10sec Max

10.3 Hand solder attachment conditions

Work in 3 seconds or less is [temperature of 350°C] possible.

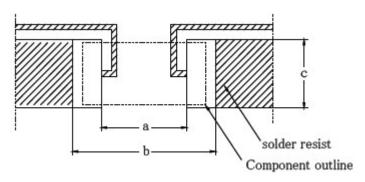
When soldering with a soldering iron, heating is performed on a land and the point does not hit the main part of a product. Please carry out.

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11. Recommended land pattern (for current sensing)
Board materials: Glass epoxy (FR-4) t=0.6mm

Thickness of copper foil: 100 um

Note: The terminal temperature should not exceed 120°C at the rated power.



Symbol	Dimensions (mm)	
а	8mΩ~:5.00	5-7m Ω : 2.80
b	14.00	
С	5.75	

- 12. Storage note
- (1) To maintain good solderability, Store the components in the temperature and humidity controlled room. Temperature: 5~35°C Humidity: 45~85% RH
- (2) Store the components at the place avoiding moisture, dust and corrosive harmful gas (hydrogen chloride, sulfurous acid gas and hydrogen sulfide) that may cause the decrease in solderability.
- (3) Store the components at the place avoiding direct sunlight.

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